

FIG. 1

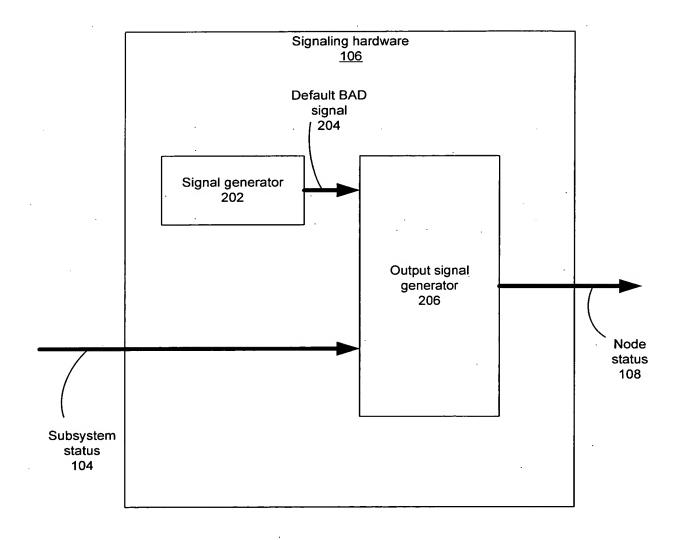


FIG. 2

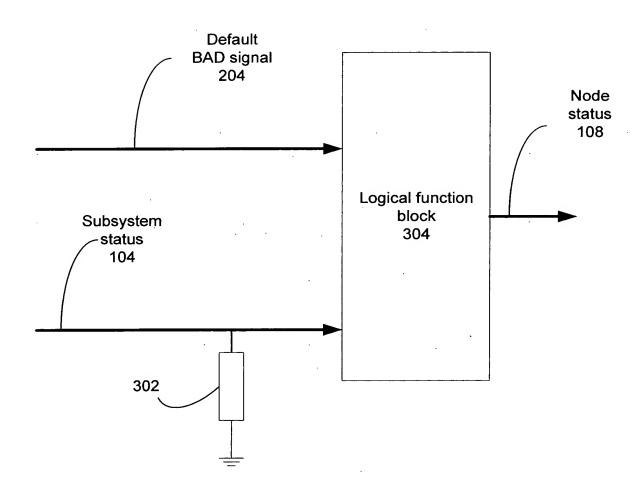
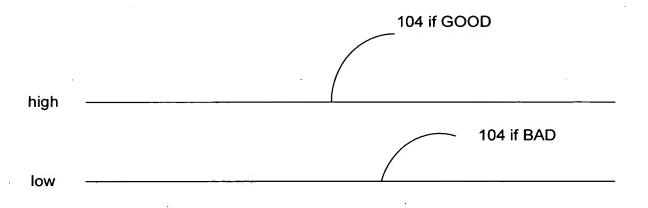


FIG. 3



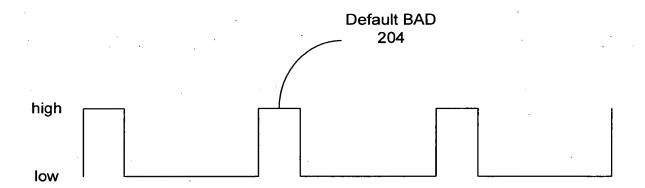
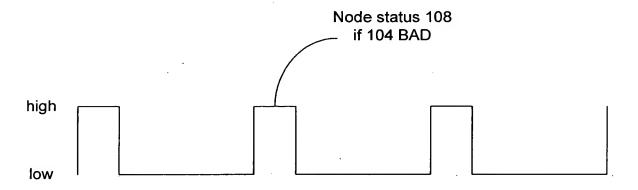


FIG. 4



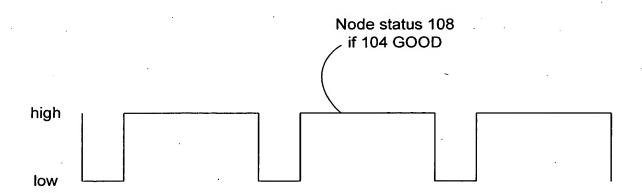
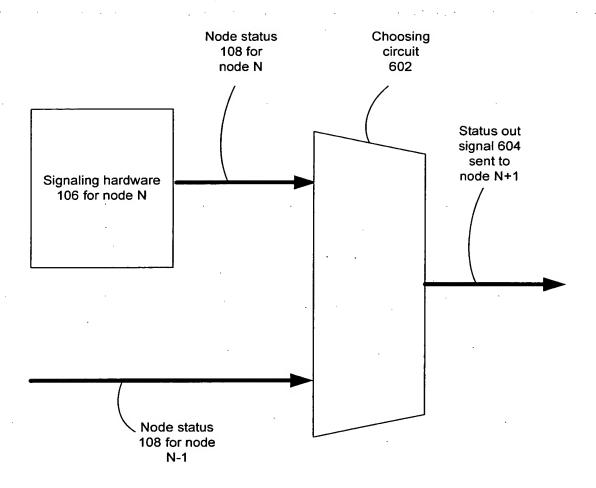


FIG. 5



Status Pass-Through Circuit FIG. 6

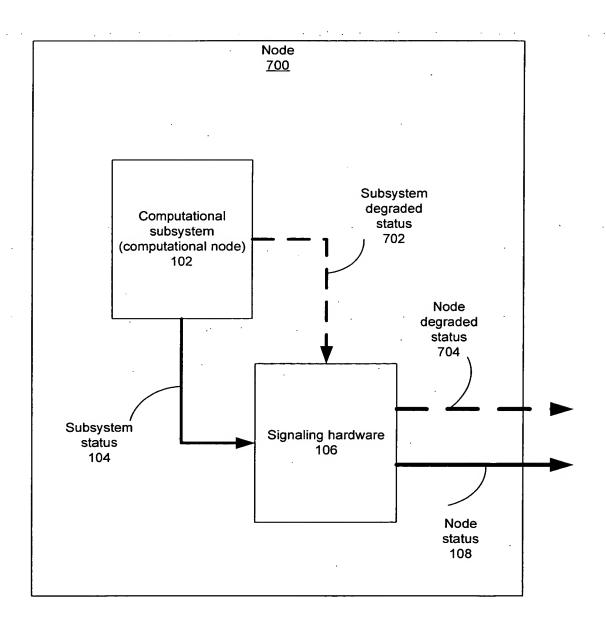
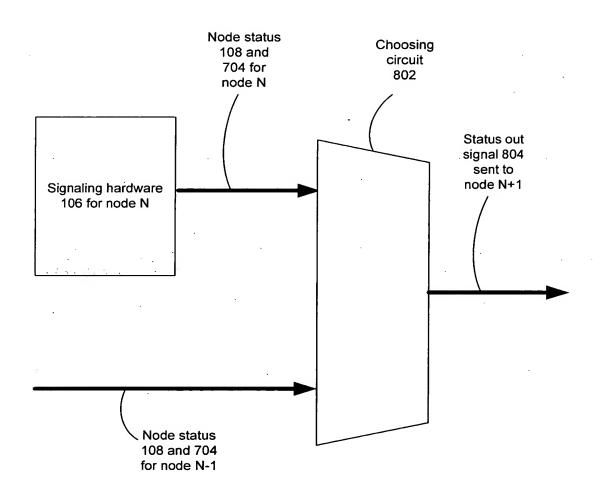


FIG. 7



Status Pass-Through Circuit FIG. 8